

Fig 1A

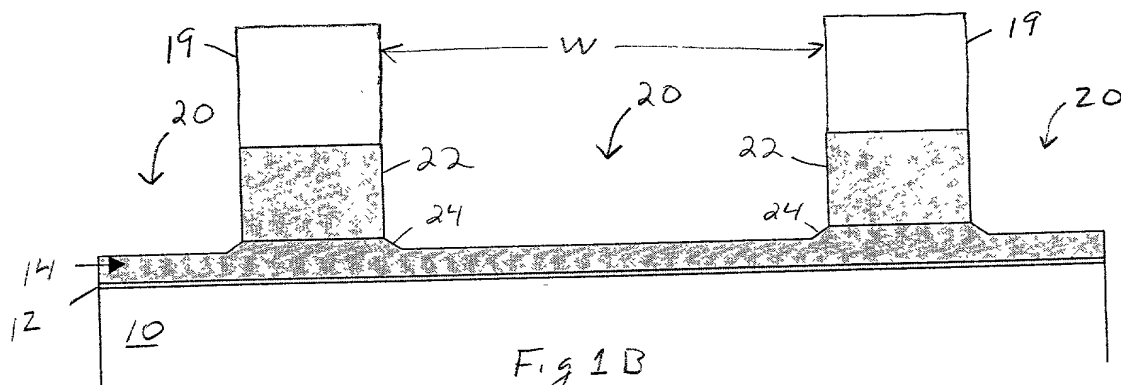


Fig 1B

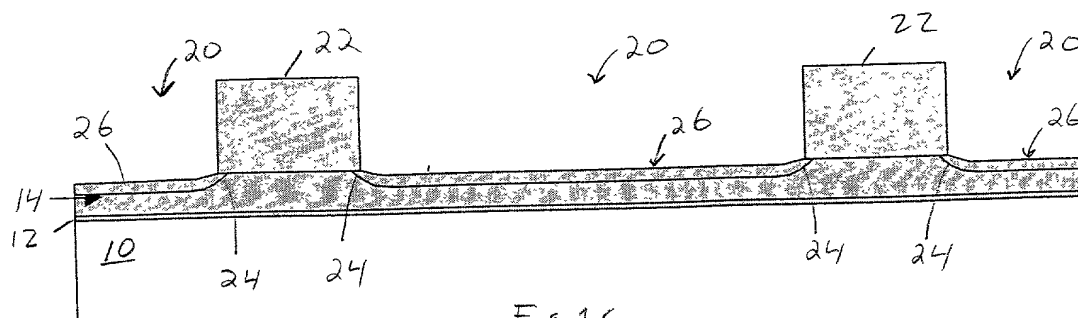


Fig 1C

109844-10781

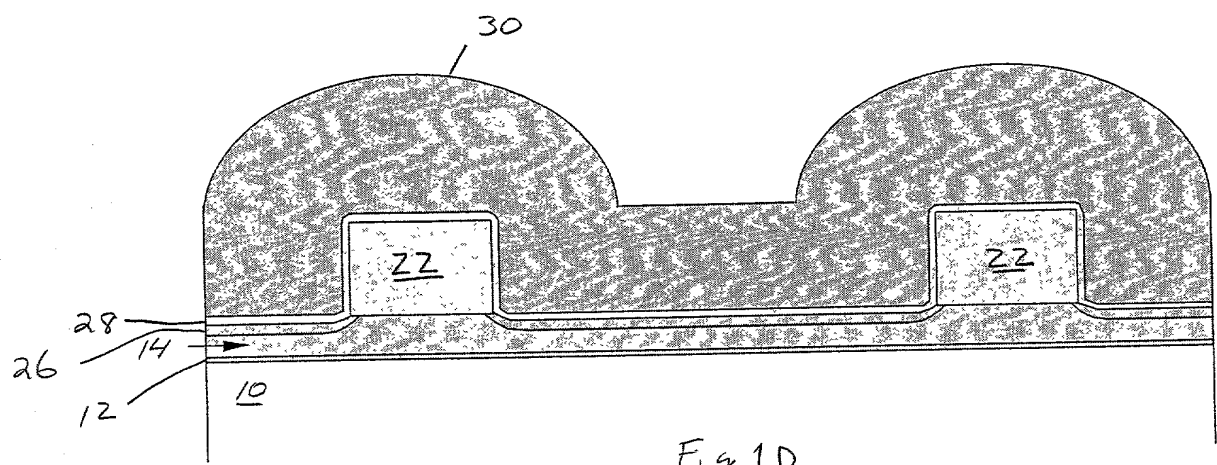


Fig 10

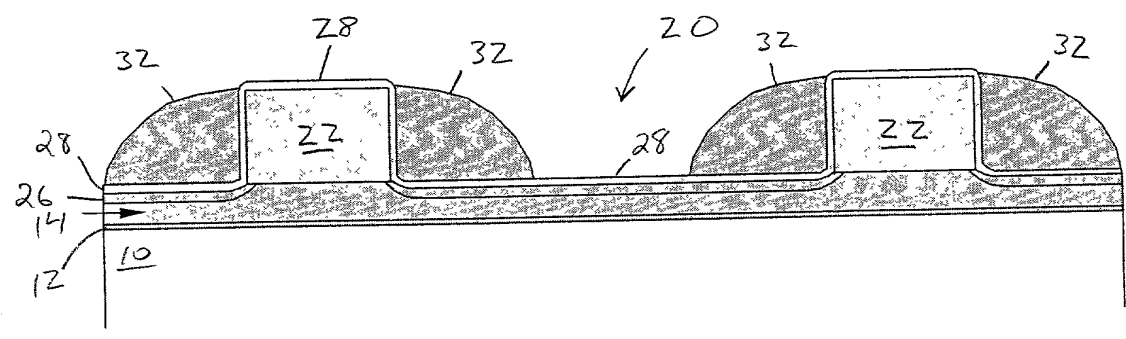


Fig 1E

099844-10101
FOI/CFR/20660

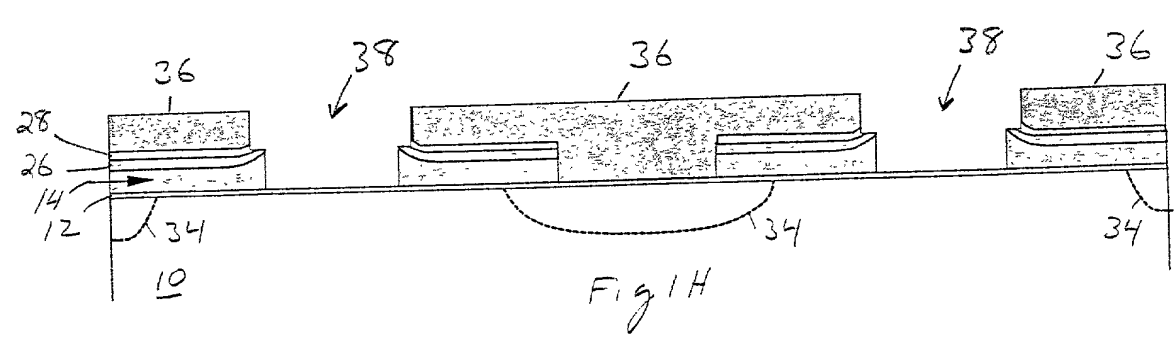
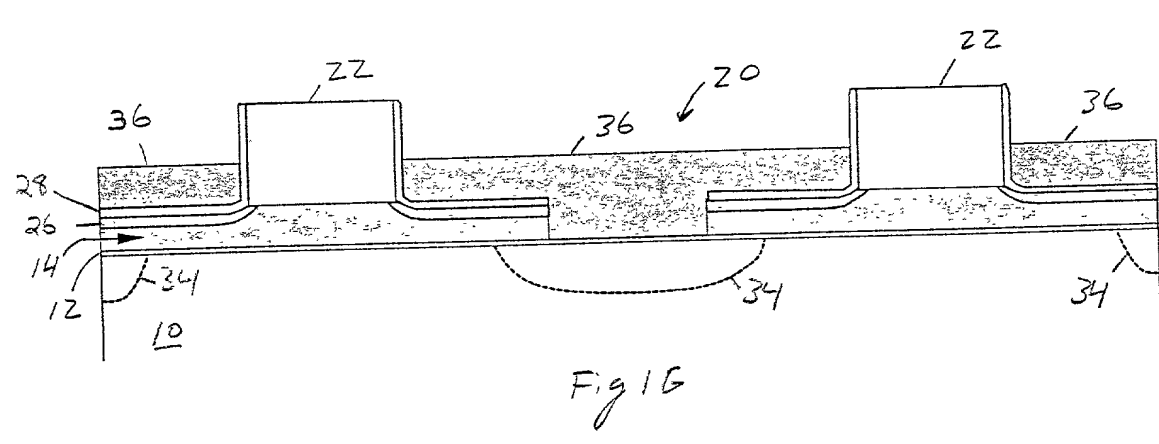
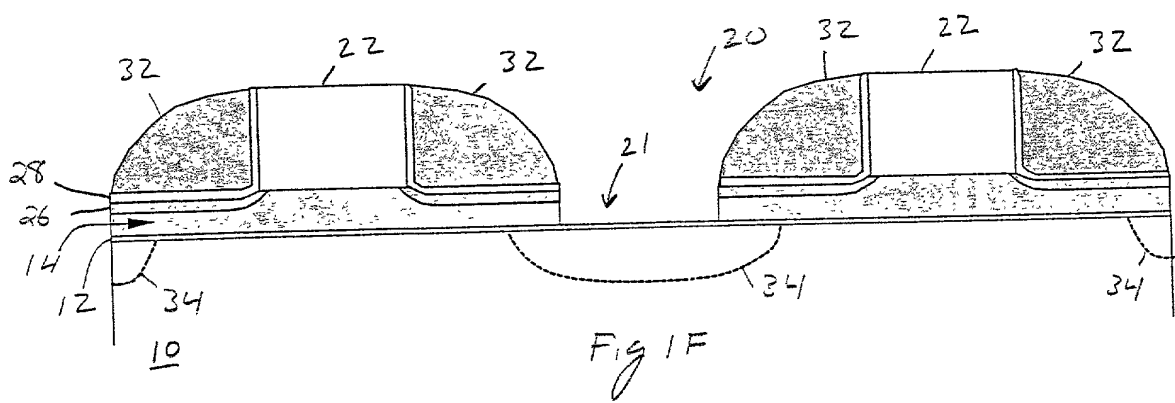
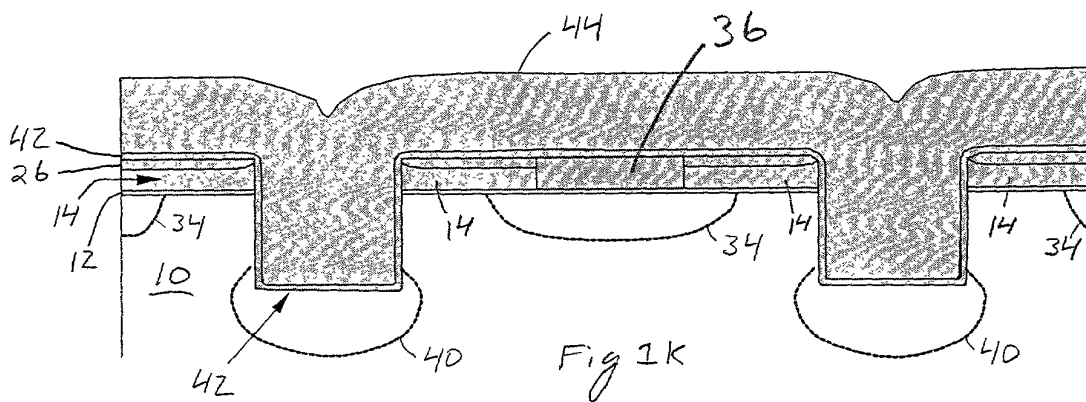
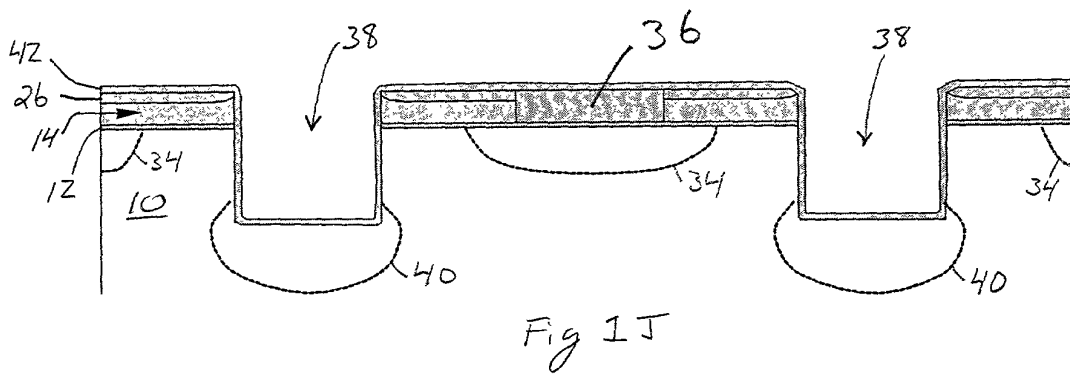
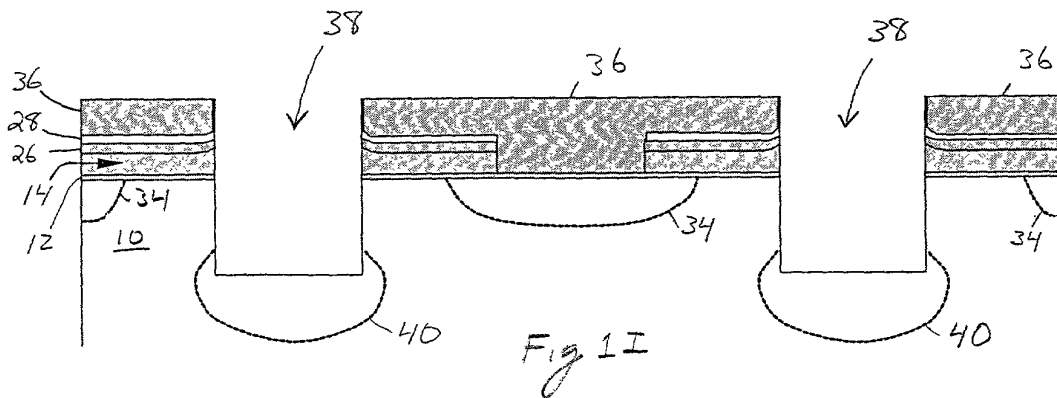


FIG. 10



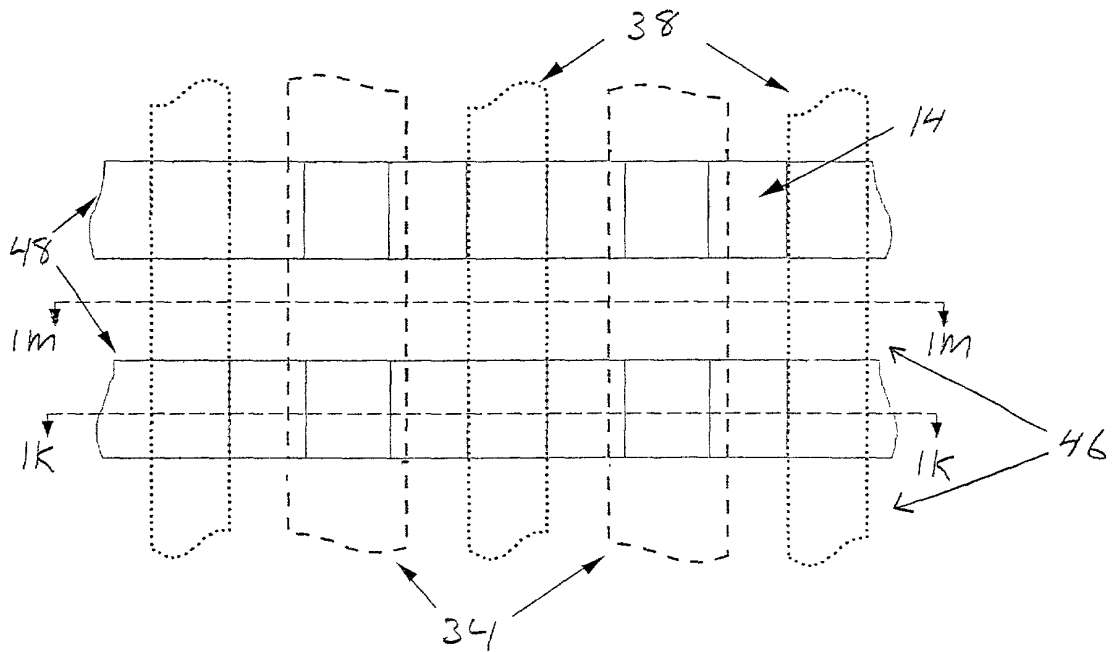


Fig 1L

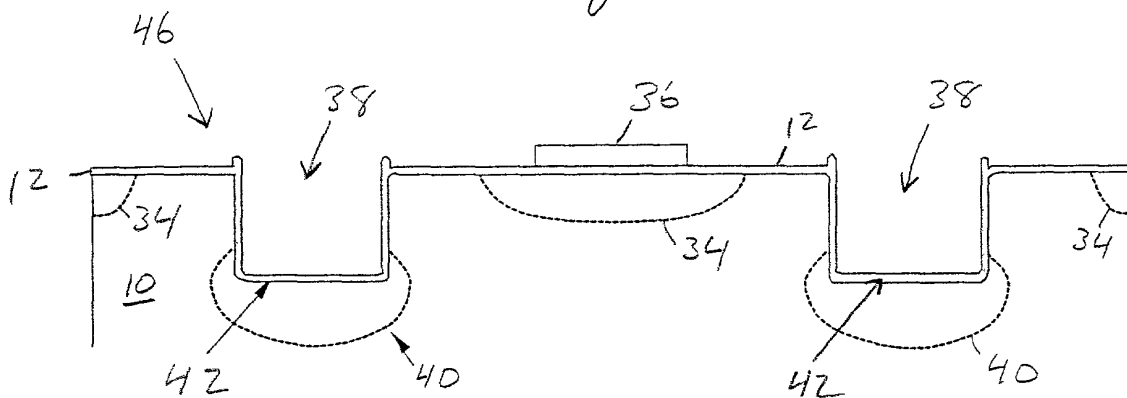
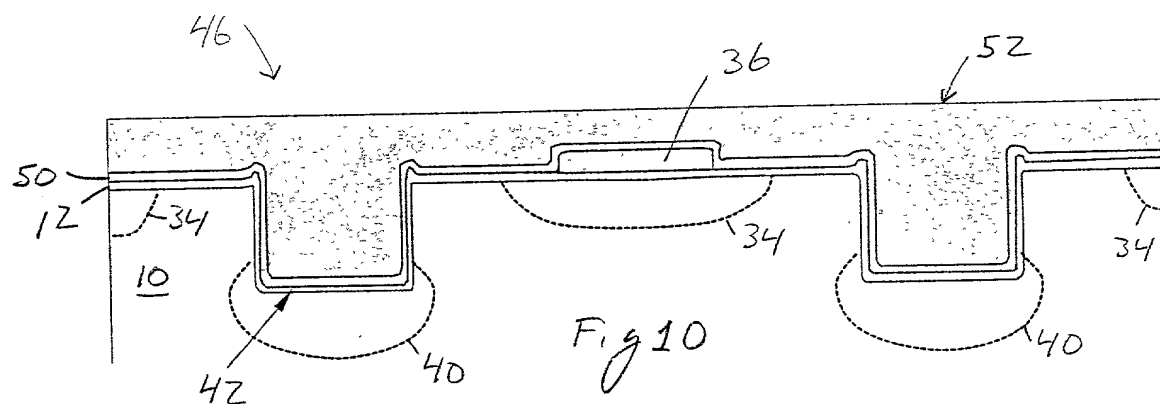
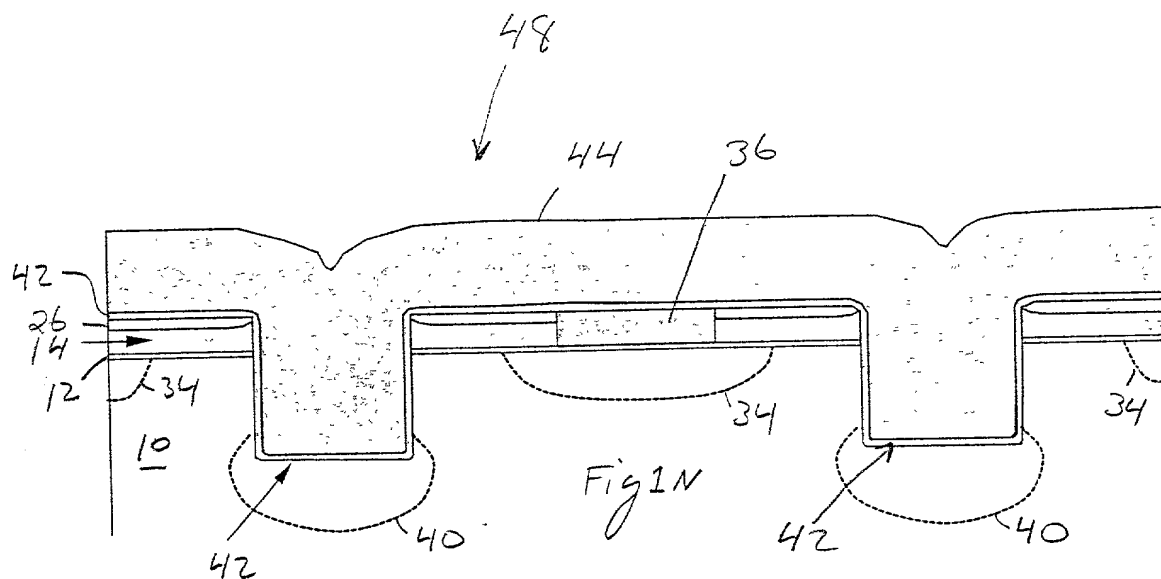


Fig 1M



09582443.101704

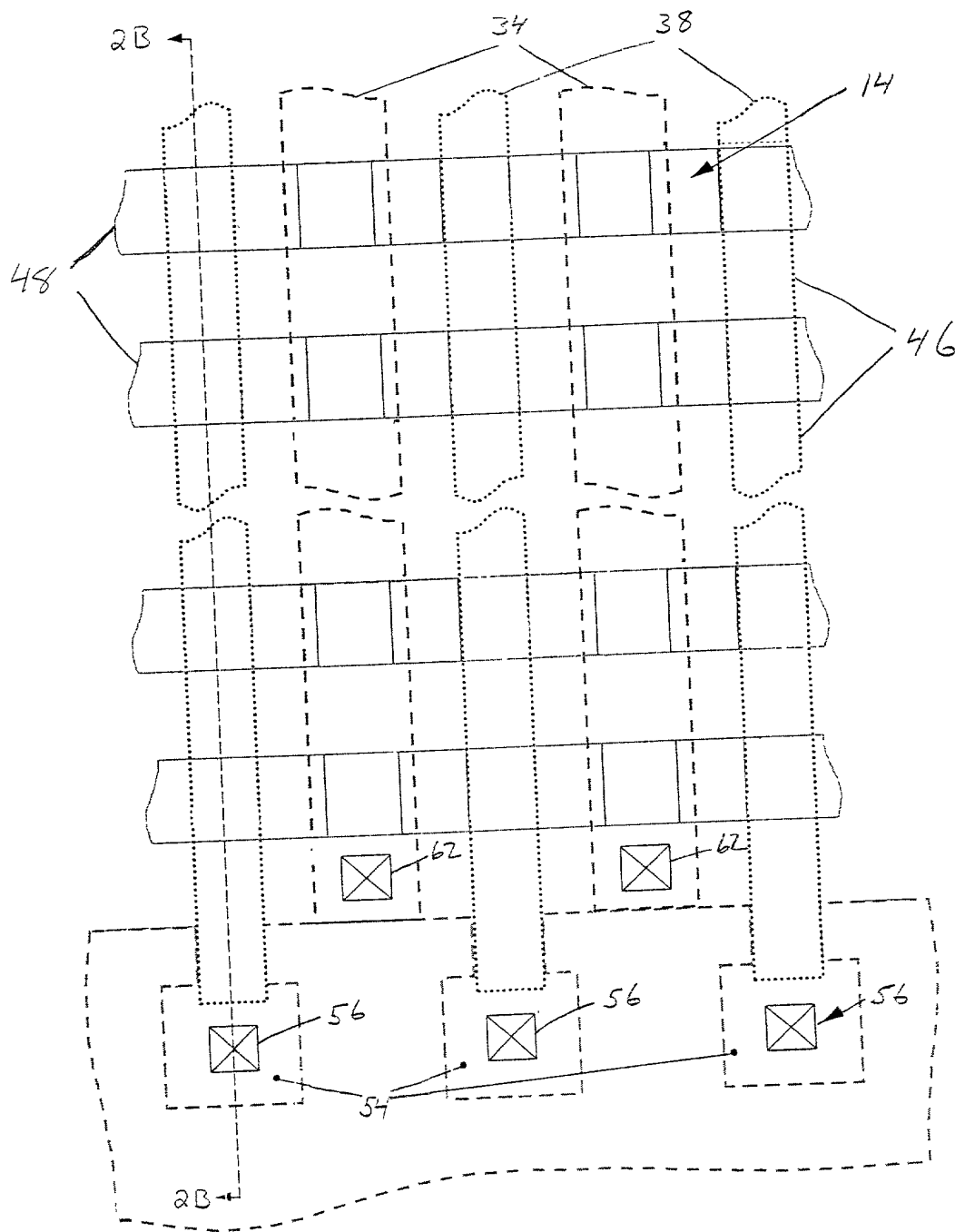


Fig 2A

090243 10101
F04T07 E142860

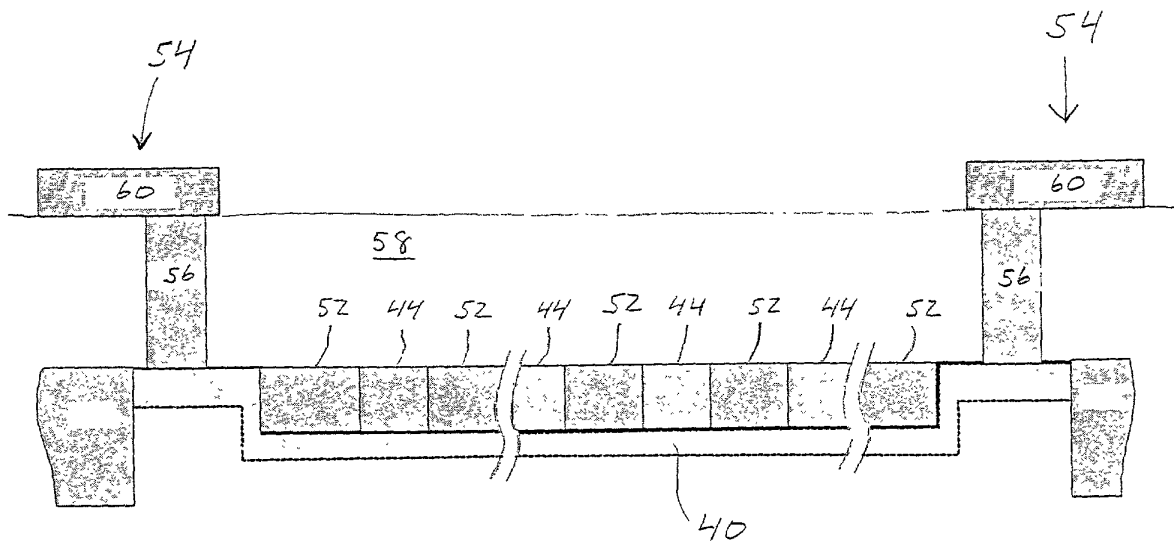


Fig 2B

Fig 3

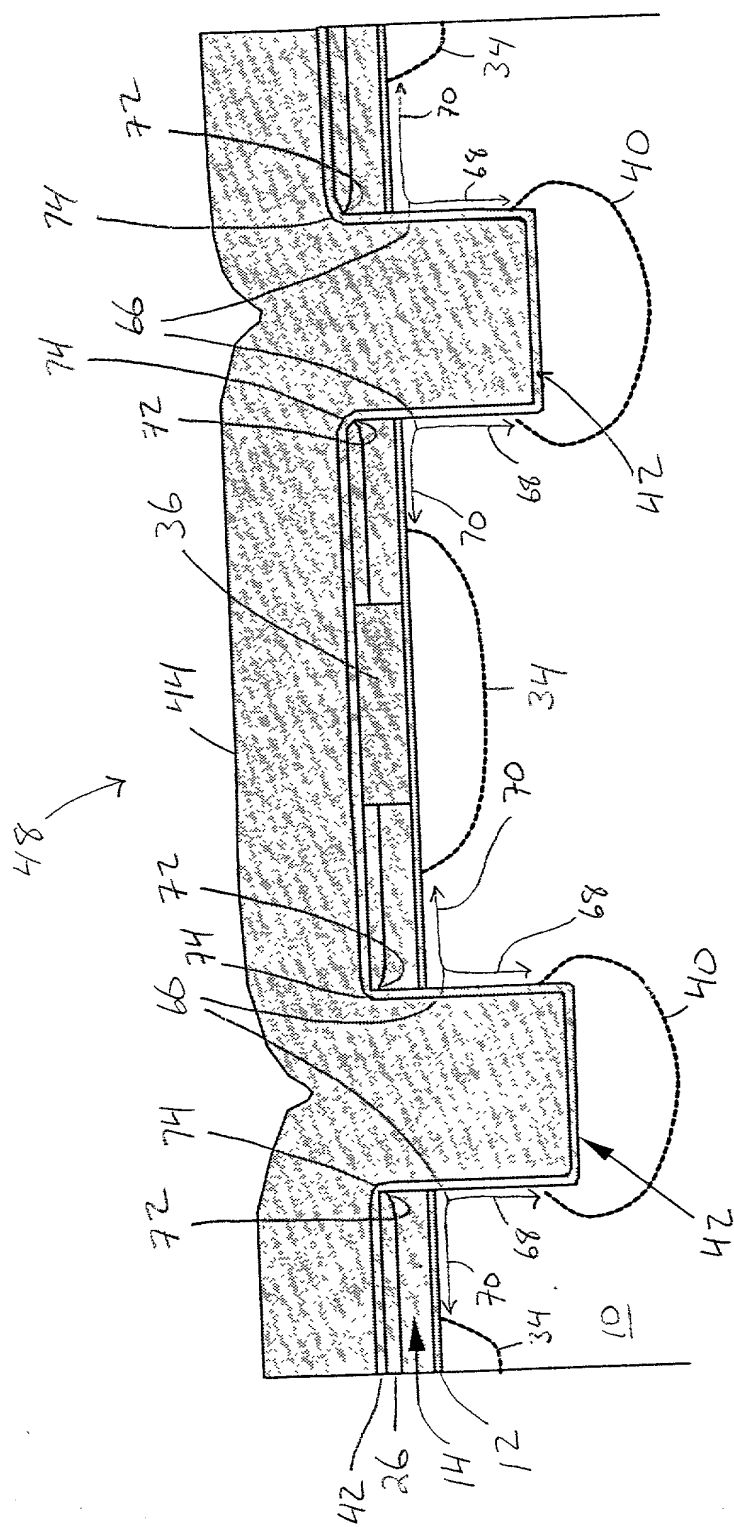


Fig 4

64

WL1

WL2

BL1

SL1

BL2

SL2

Program	Erase	Read
1.8	12	Vdd
0	0	0

<u>Program</u>	Vdd (inhibit)	Vpp (e.g. 9-10V)	Vdp (0.5-1V)	0 (inhibit)
<u>Erase</u>	0	0	0	0
<u>Read</u>	0	0	~1V	0

Fig 5

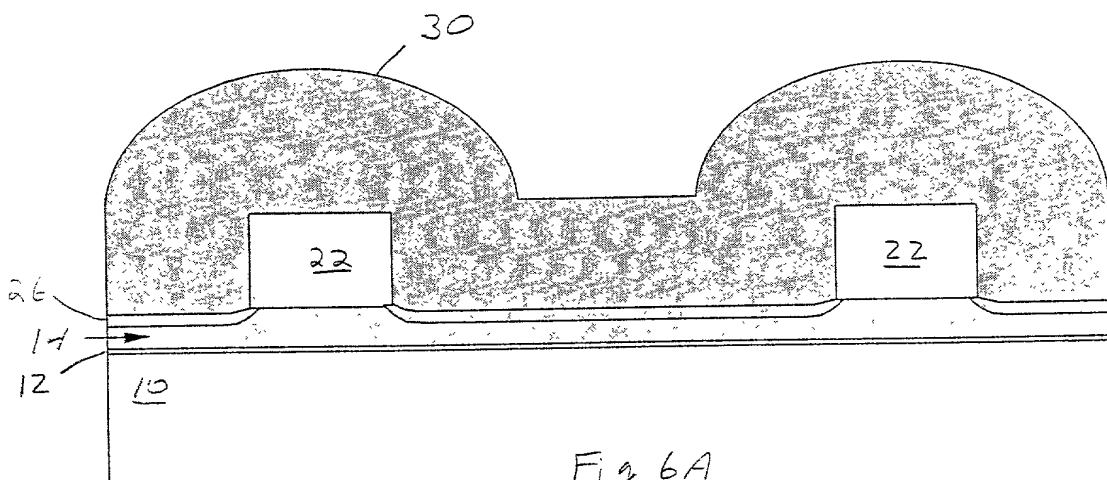


Fig 6A

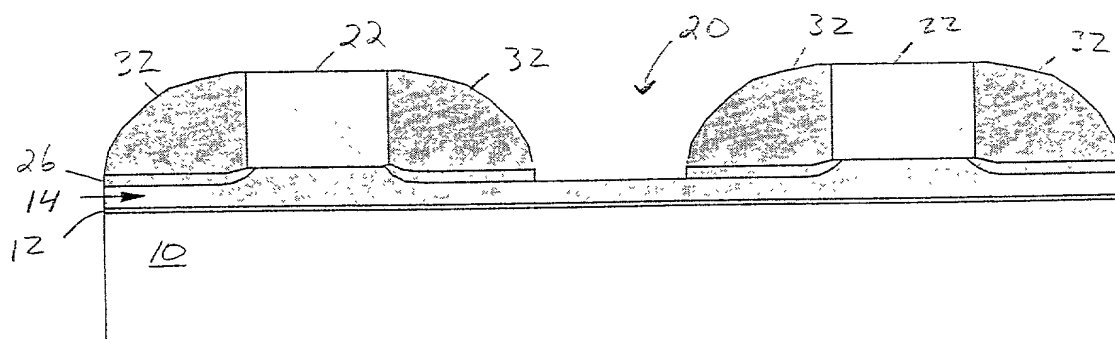
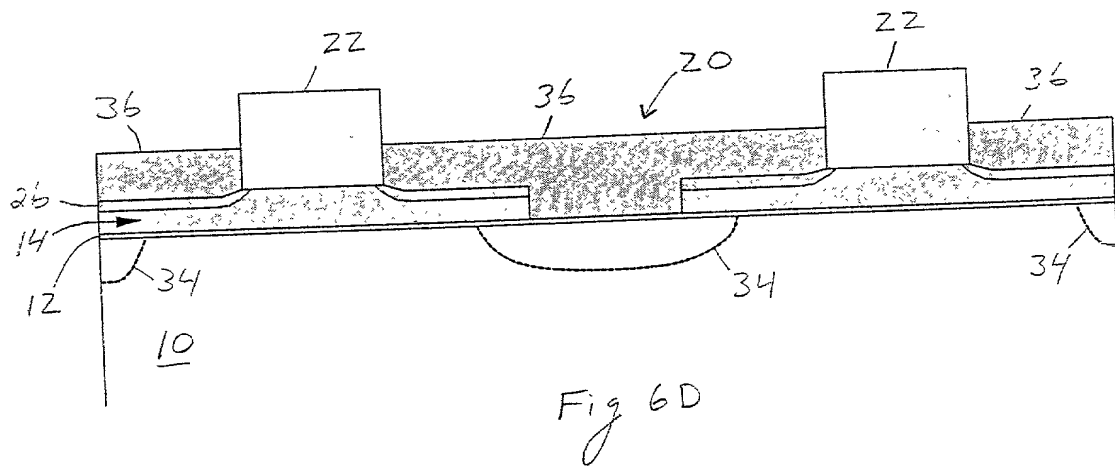
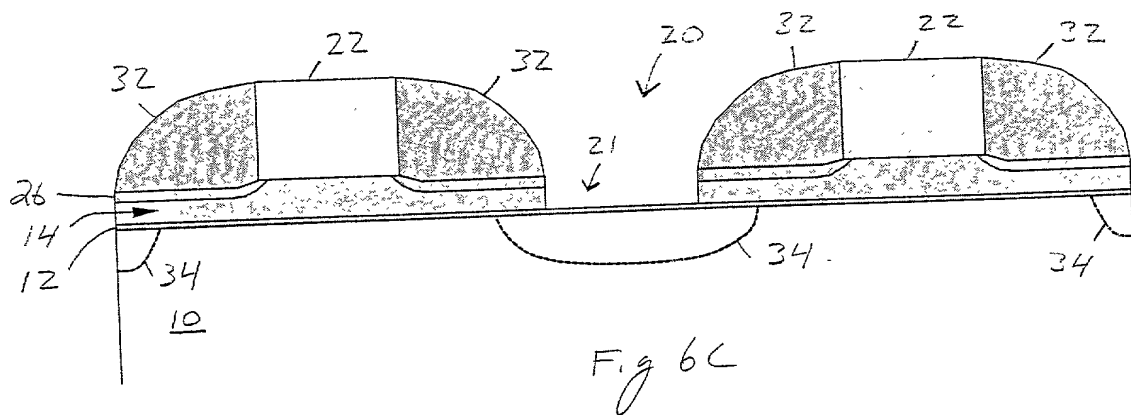


Fig 6B



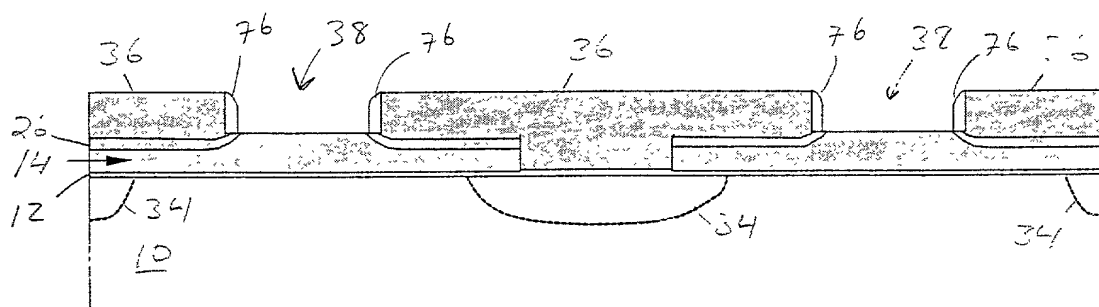


Fig 6E

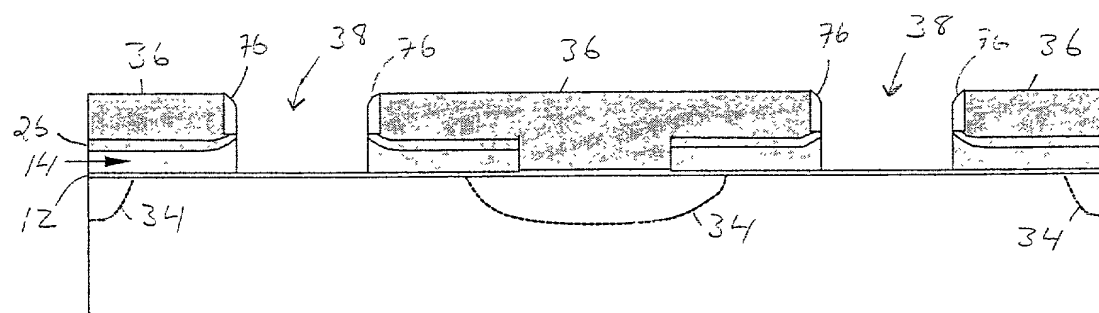


Fig 6F

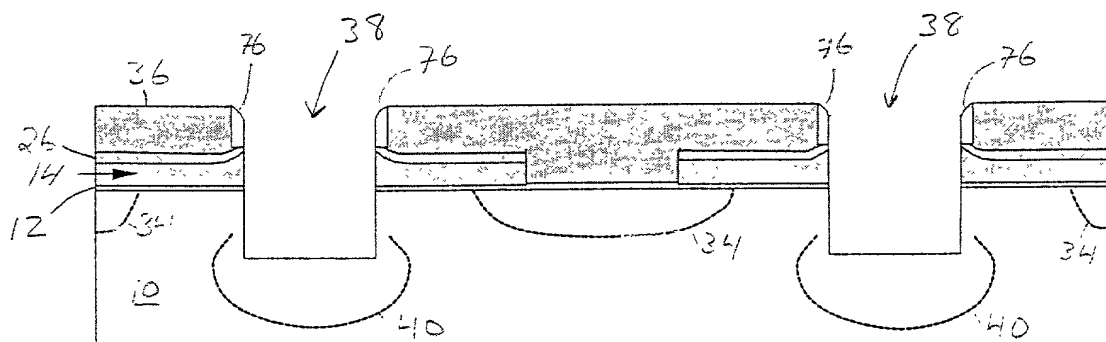


Fig 6G

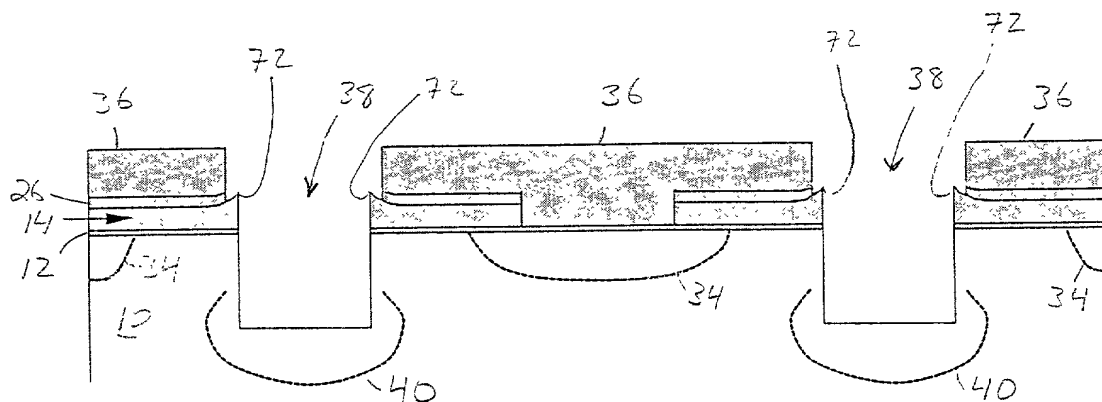


Fig 6 H

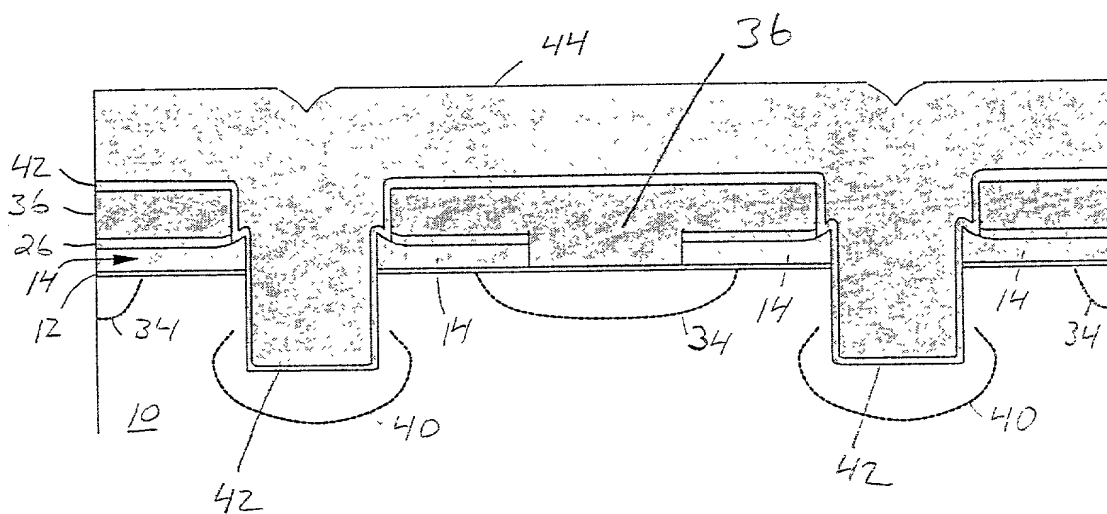
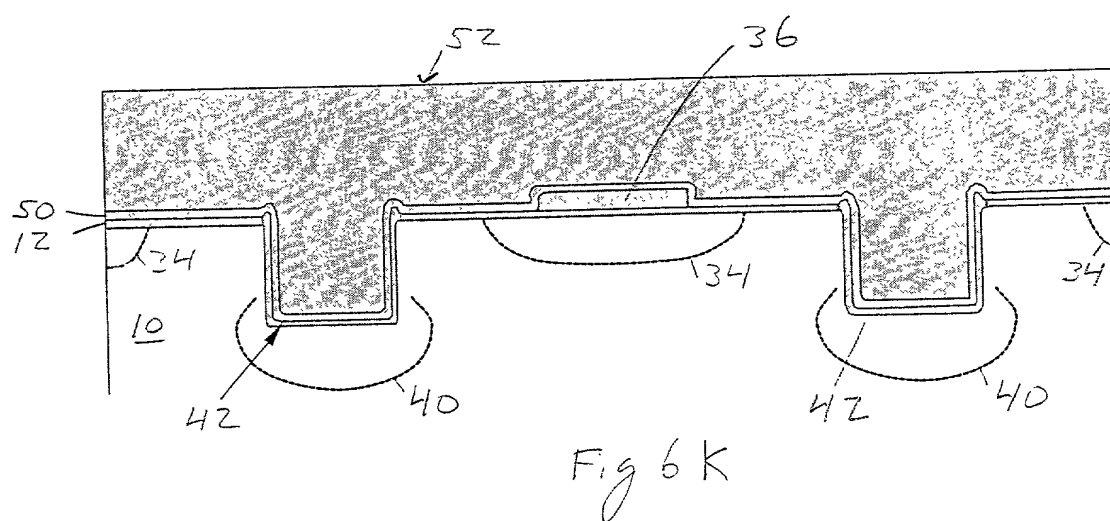
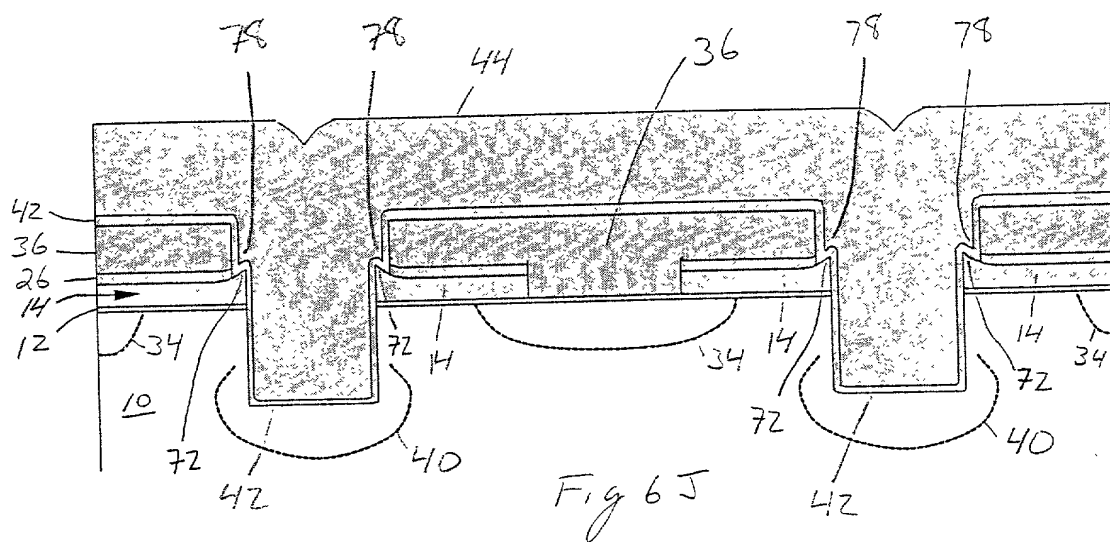


Fig 6 I



09982443.017031

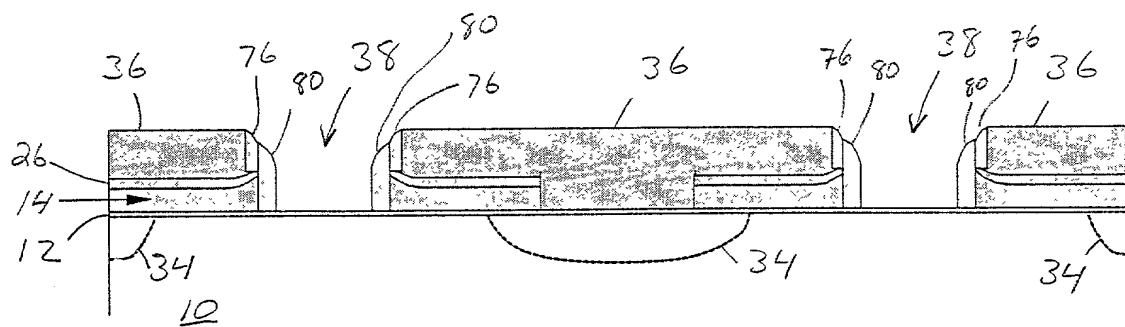


Fig 7A

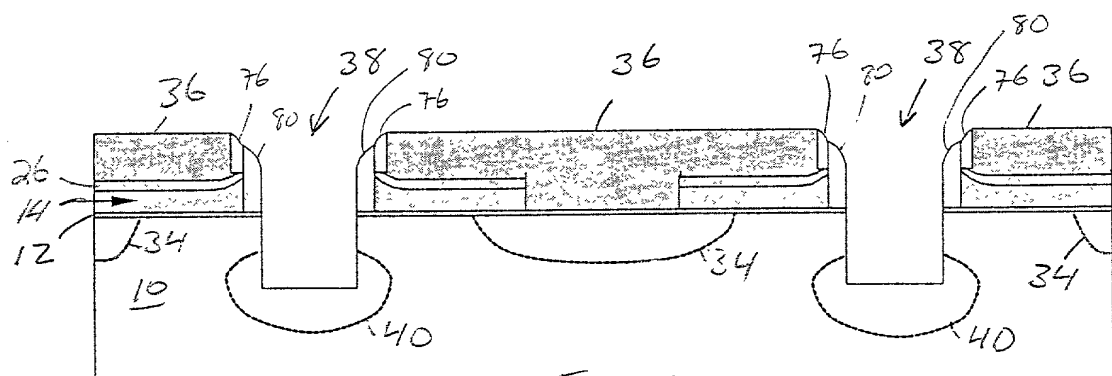


Fig 7B

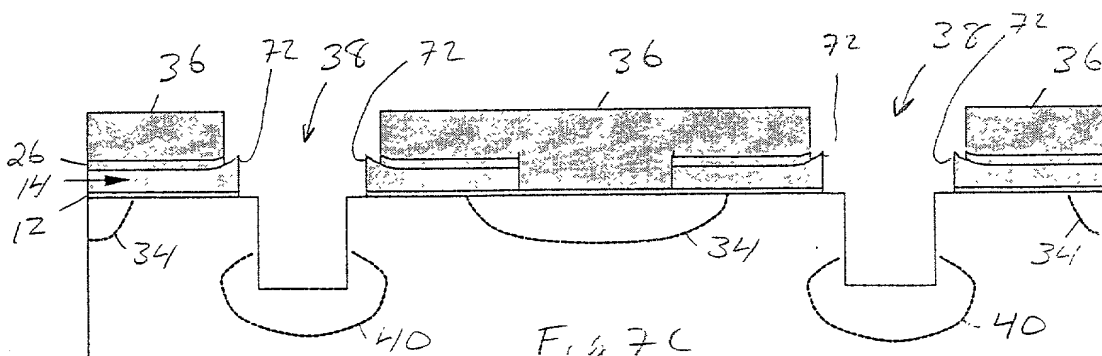


Fig 7C

0992413-101701

